

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: IWANISHI, et al.
 Docket: 10873.1199US01
 Title: METHOD FOR DESIGNING SEMICONDUCTOR INTEGRATED CIRCUIT

17613 U.S. PTO
 10/601287
 06/20/03

CERTIFICATE UNDER 37 CFR 1.10

'Express Mail' mailing label number: EV 347850799 US
 Date of Deposit: June 20, 2003
 I hereby certify that this paper or fee is being deposited with the United States Postal Service 'Express Mail Post Office To Addressee' service under 37 CFR 1.10 and is addressed to Mail Stop Patent Application, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

By: [Signature]
 Name: John Linkers

Mail Stop PATENT APPLICATION
 Commissioner for Patents
 P.O. Box 1450
 Alexandria, VA 22313-1450

The PTO did not receive the following
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But \$40.00 and \$1,008.00

Sir:

We are transmitting herewith the attached:

- ☒ Transmittal sheet, in duplicate, containing Certificate under 37 CFR 1.10.
- ☒ Utility Patent Application: Spec. 14 pgs; 3 claims; Abstract 1 pgs.
 The fee has been calculated as shown below in the 'Claims as Filed' table.
- ☒ 23 sheets of formal drawings
- ☒ Certified copy of a Japanese application, Serial No. 2002-264627, filed September 10, 2002, the right of priority of which is claimed under 35 U.S.C. 119
- ☒ A signed Combined Declaration and Power of Attorney
- ☒ Assignment of the invention to Matsushita Electric Industrial Co., Ltd., Recordation Form Cover Sheet
- ☒ A check in the amount of \$750.00 to cover the Filing Fee
- ☒ A check for \$40.00 to cover the Assignment Recording Fee.
- ☒ Application Data Sheet, 4 pages.
- ☒ Other: Communication Regarding Requested Figure
- ☒ Return postcard

CLAIMS AS FILED

| Number of Claims Filed | In Excess of: | Number Extra | Rate | Fee |
|-------------------------------------|---------------|--------------|---------|----------|
| Basic Filing Fee | | | | \$750.00 |
| Total Claims | | | | |
| 3 | 20 | 0 | x 18.00 | \$0.00 |
| Independent Claims | | | | |
| 3 | 3 | 0 | x 84.00 | \$0.00 |
| MULTIPLE DEPENDENT CLAIM FEE | | | | \$0.00 |
| TOTAL FILING FEE | | | | \$750.00 |

Please charge any additional fees or credit overpayment to Deposit Account No. 13-2725. A duplicate of this sheet is enclosed.

MERCHANT & GOULD P.C.
 P.O. Box 2903, Minneapolis, MN 55402-0903
 (612) 332-5300

By: [Signature]
 Name: Douglas P. Mueller
 Reg. No.: 30,300
 Initials: DPM/pjk



TO TRANSMITTAL - NEW FILING)

The PTO did not receive the following
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S/N Unknown

PATENT

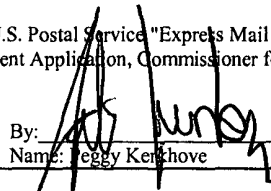
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

| | | | |
|-------------|---|-----------------|----------------|
| Applicant: | Iwanishi, et al. | Examiner: | Unknown |
| Serial No.: | Unknown | Group Art Unit: | Unknown |
| Filed: | Herewith | Docket No.: | 10873.1199US01 |
| Title: | METHOD FOR DESIGNING SEMICONDUCTOR INTEGRATED CIRCUIT | | |

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By: 
Name: Peggy Kerkhove

COMMUNICATION REGARDING REQUESTED FIGURE

Mail Stop New App
Assistant Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450


Dear Sir:

In connection with the above-identified application, applicant respectfully requests that Figure 1 be printed on the front page of the published application.

If a telephone conference would be helpful in resolving any issues concerning this communication, please contact Applicants' primary attorney-of record Douglas P. Mueller (Reg. No. 30,300), at (612) 371.5237.

Respectfully submitted,
MERCHANT & GOULD P.C.
P.O. Box 2903
Minneapolis, Minnesota 55402-0903
(612) 332-5300

Dated: June 20, 2003

By: 
Douglas P. Mueller
Reg. No. 30,300

DPM/pjk

